

Acceptance ratio: 18.18%

No	TITLE	Details
1	Inverting Buck-Boost DCDC Converter Design Challenges	Accepted
2	Design Of Standard And Custom Peripheral Using Nios Ii Processor	Rejected
3	FPGA Implementation of ASK & FSK Modulator Based on Matlab/Xilinx System Generator	Rejected
4	Research of Automatic Meter Reading System Based on Broadband Carrier in the Power Line	Rejected
5	Generic Socp Platform For Video Interactive System With Mpmc Controller	Accepted
6	Design and Implementation of An Embedded System for online Measurement of Concentration of Environmental Carbon Dioxide Gas	Rejected
7	An Efficient Reconfigurable System With Nios Ii Processor	Rejected
8	Rfid Based Automatic Controlled Robotic Arm For Library Automation	Rejected
9	Novel Approach In Piezoelectric Power Generation for Computer Accessory	Rejected
10	Embedded Based Wind Mill Direction Using Object Sensor	Rejected
11	Verification of Transport Protocols's Parallel Routing of A Vehicle Gateway System	Rejected