Acceptance rate: 10.3%

NO	TITLE	Details
1	**Evaluating and Providing Solutions for Business plan implementation**	reject
2	**The Role of Outsourcing in Improving the Performance, Based on Balanced Score Card (Case study: Ilam Post Office)**	reject
3	**A Novel Method for Accurate Motion Detection and Tracking using Modified BMM method**	reject
4	**ADAPTIVE FILTER IMPLEMENTATION USING FPGA**	reject
5	**DETECTION OF SUDDEN PEDESTRIAN CROSSINGS FOR DRIVING ASSISTANCE SYSTEMS**	reject
6	**Moving obstacle detection and remote video monitoring system using Cortex-A8**	reject
7	**Development of Telugu Text To Speech System on Beagle Board**	reject
8	**Low Power Irrigation Monitoring System Using Msp430 Micro Controller**	reject
9	**SPEECH TO TEXT CONVERSION SYSTEM ON BEAGLE BOARD**	reject
10	**NOVEL DESIGN FLOW FOR INTRINSIC EVOLVABLE HARDWARE RECONFIGURATION**	reject
11	**IMPLEMENTATION OF 70MHz DIGITAL RECEIVER ON FPGA FOR RADAR APPLICATIONS**	reject
12	**Driver Drowsiness Detection System Using Accelerometer and MSP430**	reject
13	**RAILWAY TRACK FINDING SYSTEM WITH RFID APPLICATION**	reject
14	**SOCS BASED OPENRISC AND MICROBLAZE SOFT PROCESSORS COMPARISON STUDY CASES: AUDIO IMPLEMENTATION AND NETWORK IMPLEMENTATION BASED SOCS**	Accept
15	**RFID in Traffic Management**	reject
16	**Real-Time Scheduler for Wireless Sensor Network: a Review**	reject

17	**Real Time Power Generation Using Piezoelectric Ceramic Disc for Low Voltage Appliances**	reject
18	**Development of Soft-Core processor system on FPGA**	reject
19	**Quartz Crystal for Thin film Thickness Measurement Using Embedded Software (PSoC)**	reject
20	**Development of Telugu Text To Speech System using OMAP3530**	reject
21	**FPGA Accelerated System Development for Rolling Bearings Fault Detection of Induction Motor**	reject
22	**Design of a WHITE-LEDs DRIVER Backlight Boost Converter**	reject
23	**Heart Rate measurement using pulsed method**	reject
24	**PERFORMING AN EXPERIMENTAL PLATFORM TO OPTIMIZE DATA MULTIPLEXING**	Accept
25	**ANALYSIS AND IMPLEMENTATION OF PARALLEL CAUSAL BIT PLANE CODING IN JPEG2000 STANDARD**	Accept
26	**Study and Design of Wireless Exchange Based on RF Module**	reject
27	**Design and implementation of equiripple FIR high pass filter on FPGA**	reject
28	**RTOS based control system design for Hard Real time systems**	reject
29	**MCS-51 Microcontroller Based Industrial Automation and Control System using CAN protocol**	reject